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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OSHIMA, Tadashi**

Group Art Unit: 2814

Serial No.: **09/996,758**

Examiner: **PHAM, Hoai V.**

Filed: **November 30, 2001**

P.T.O. Confirmation No.: 4892

For: **SEMICONDUCTOR DEVICE WITH TWO TYPES OF FET'S HAVING  
DIFFERENT GATE LENGTHS AND ITS MANUFACTURE METHOD**

**RESPONSE TO THE RESTRICTION REQUIREMENT**  
**DATED NOVEMBER 6, 2002**

Commissioner for Patents  
Washington, D.C. 20231

Date: November 21, 2002

Sir:

This paper is submitted in response to the Official Action dated **November 6, 2002**.

In the Action, restriction is required between Group (I), Claims 4-6, 9 and 10, drawn to a semiconductor device; and Group (II), Claims 1-3, 7 and 8, drawn to a method of making a semiconductor device.

Applicant hereby elects the subject matter of Group (I), Claims 4-6, 9 and 10, and the Embodiment 1 as shown in Figures 1-3 for prosecution in this application. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

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In the event any additional fees are required in connection with this response, please  
charge our Deposit Account No. 01-2340.

Respectfully submitted,

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